

**AMENDMENTS TO THE DRAWINGS:**

Figures 1 and 2 have been amended to include legends.

## **REMARKS**

Reconsideration and allowance of the present application are respectfully requested. Claims 1-8 remain pending in the application.

In numbered paragraph 1 on page 2 of the Office Action, the drawings are objected to because the reference character "8" has been used twice. As recognized by the Examiner, the reference numeral "8" has been used to designate the same PCI bus at two different locations such that correction to the drawings is unnecessary.

In numbered paragraph 2, Figures 1 and 2 are objected to as failing to contain suitable legends. Accordingly, submitted herewith is a corrected drawing along with a replacement sheet. Withdrawal of the objections to the drawings is therefore requested. The proposed changes to the Figures also obviate the objections raised in numbered paragraph 3 on page 3 of the Office Action.

In numbered paragraph 4, the Examiner has indicated that a certified copy of the priority document has not yet been supplied. Applicant is in the process of obtaining a certified copy of the Czech priority document, and will forward it to the Patent Office upon receipt.

In numbered paragraph 5, the specification is objected to because the detailed description does not refer to the drawings by Figure number. By the foregoing amendment, the specification has been amended to refer to the Figure numbers. Withdrawal of this objection is therefore requested.

In numbered paragraphs 6-9, the specification is objected to as failing to provide an enabling disclosure of subject matter recited in claim 3. The Examiner asserts that claim 3 can be interpreted as reciting three processors, and requests

that claim 3 be amended "in such a way as to clearly claim only the processors discussed in the specification." By the foregoing amendment, claim 3 has been amended to clarify that "the processor" in line 2 corresponds to the "processor" recited in line 4. Withdrawal of the objection to claim 3, and to the specification under 35 U.S.C. §112, first paragraph, is therefore requested.

In numbered paragraphs 10-19, claims 3-8 are rejected under 35 U.S.C. §112, second paragraph. By the foregoing amendment, claims 3-8 have been amended to address the issues raised by the Examiner. Withdrawal of the objections under 35 U.S.C. §112, second paragraph, is therefore requested.

In numbered paragraphs 21-22, claims 1-3 are rejected as being unpatentable over U.S. Patent No. 5,692,211 (Gulick) in view of U.S. Patent No. 6,374,389 (Tuma et al).

In paragraph 23, claim 4 is rejected under 35 U.S.C. §103(a) as being unpatentable over the Gulick and Tuma patents in further view of U.S. Patent No. 5,386,385 (Stephens, Jr.)

In numbered paragraph 24, claim 5 is rejected as being unpatentable over Gulick and Tuma in further view of U.S. Patent Publication No. US 2001/0018728 (Topham et al). In paragraph 25, claim 6 is rejected as being unpatentable over Gulick and Tuma in further view of U.S. Patent No. 6,003,017 (Rang et al).

In numbered paragraph 26, claim 7 is rejected as being unpatentable over Gulick, Tuma and Rang in further view of U.S. Patent No. 5,388,267 (Chan et al).

In numbered paragraph 27, claim 8 is rejected as being unpatentable over Gulick and Tuma in further view of U.S. Patent No. 5,185,876 (Nguyen et al).

The foregoing rejections are respectfully traversed, as none of the documents relied upon by the Examiner, either individually or in the various combinations set forth in the Office Action, teach or suggest features recited in Applicant's independent claims 1 and 3.

Claim 1 is directed to a control unit comprising a PCI bus interface, a SCSI bus interface, and program equipment for connecting a PCI bus and a SCSI bus to an electronic semiconductor disc of a computing system. Claim 3 is directed to a computing system with an electronic semiconductor disc and with a processor. The processor is connected by a PCI bus to a PCI adapter, which is linked through the PCI bus to the semiconductor disc connected by a local bus to the processor. The PCI adapter, such as PCI adapter 4 of the exemplary Figure 1 embodiment, includes a unit (e.g., unit 11) of a programmable SCSI control unit. The unit 11 is connected both to an interface of the PCI bus for communicating with the electronic semiconductor disc, and to an interface (such as interface 12) of the SCSI bus for communicating with an external computing system SCSI control unit. Such combinations of features are neither taught nor suggested by the documents relied upon by the Examiner.

Figure 1 of the Gulick patent discloses a PCI bus 120 and a SCSI adapter 126. However, this patent does not teach or suggest program equipment included with the SCSI adapter 126 for connection of the PCI bus and/or SCSI adapter to a semiconductor disc memory.

The Tuma patent discloses a solid-state disk emulator, but does not teach or suggest use of program equipment for connecting a PCI bus and a SCSI bus to an electronic semiconductor disc. As such, there would have been no motivation or

suggestion to have combined the Gulick and Tuma patents to arrive at the presently claimed invention. Even if these patents could have been combined, the presently claimed invention would not have resulted. Neither patent teaches or suggests program equipment for connecting a PCI bus and a SCSI bus to an electronic semiconductor disc (Claim 1), or a PCI adapter which includes a unit of a programmable SCSI unit, connected both to an interface of the PCI bus for communicating with the electronic semiconductor disc, and to an interface of the SCSI bus for communicating with an external computing system SCSI control unit (Claim 3).

The remaining patents cited by the Examiner fail to overcome the deficiencies of the Gulick and Tuma patents, such that independent claim 1 and independent claim 3 are considered allowable. The remaining claims recite additional advantageous features which further distinguish over the documents relied upon by the Examiner, and are also considered allowable.

All objections and rejections raised in the Office Action having been addressed, it is respectfully submitted that the application is in condition for allowance and a Notice of Allowance is respectfully solicited.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

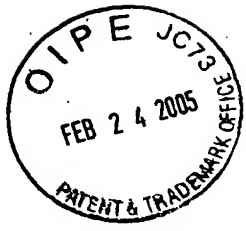
Date: February 24, 2005

By: 

Patrick C. Keane

Registration No. 32,858

P.O. Box 1404  
Alexandria, Virginia 22313-1404  
(703) 836-6620



REDACTED DRAWING

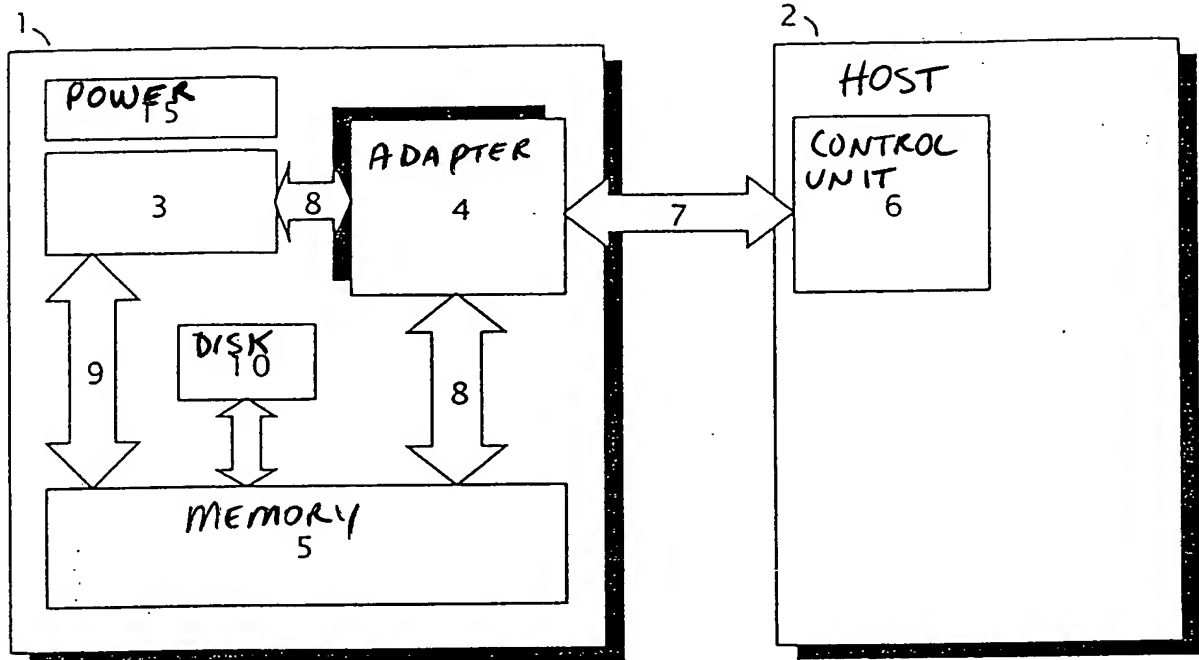


FIG. 1

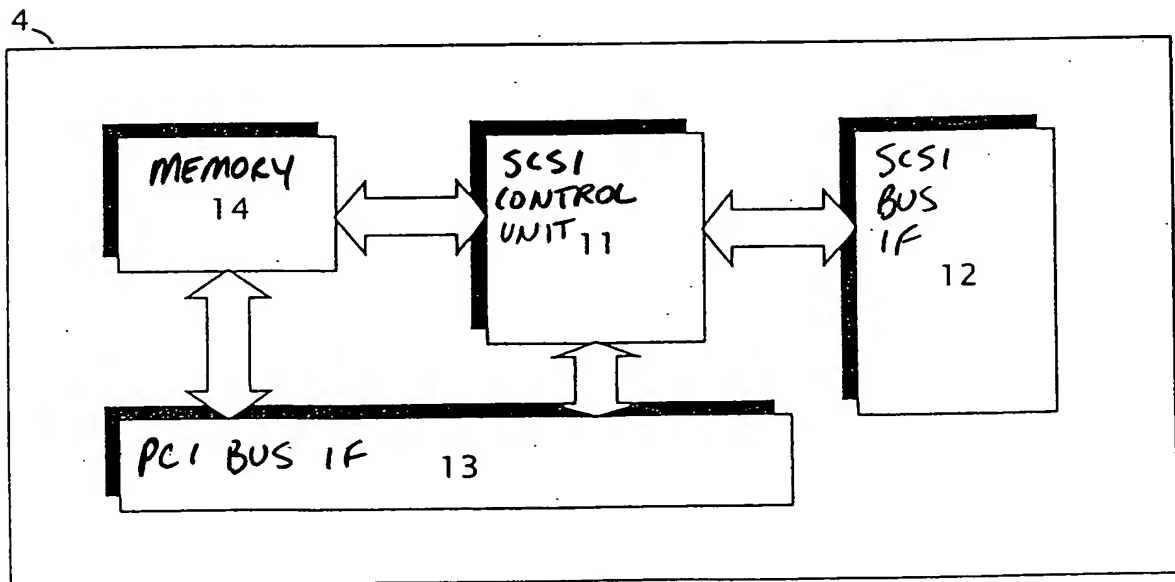


FIG. 2